

Clock v122 karltrn.com/clock december 15th 2021

Step	Qty	Reference(s)	Value	Footprint	Notes
1	1	D1	1N5819	DO-41	check orientation, power protection optional w/ shrouded header
2	6	Q1, Q2, Q3, Q4, Q5, Q6	BC847	SOT-23 marked 1F	any NPN. check orientation, should see text on top
3	1	U1	4013	SOIC-14_3.9x8.7mm_P1.27mm	check orientation
4	1	U2	NE555P	SOIC-8_3.9x4.9mm_P1.27mm	check orientation
5	1	U3	4017	SOIC-16_3.9x9.9mm_P1.27mm	check orientation - its different
6	4	C1, C2, C4, C6	100n	C_0805_2012	
7	1	C3	10u	C_0805_2012	sets timing range
8	1	C5	10u	Cap_THT:D6.3mm_P2.50mm	power decoupler
9	1	D6	FDLL4148	D_1206_3216	check orientation
10	5	R1, R2, R3, R4, R12	100K	R_0805_2012	labeled 1003
11	5	R13, R15, R16, R19, R21	10K	R_0805_2012	labeled 1002
12	10	R5, R6, R7, R8, R9, R14, R17, R18, R20, R22	1K	R_0805_2012	labeled 1001
13	2	R10, R11	100R	R_0805_2012	labeled 1000
14	1	J6	Header	power 2x05	check orientation
fit steps 15-19 with faceplate before soldering					
15	1	RV1	B10K	Alpha_RD901F-40-00D	fine tune could be top or bottom
16	1	RV2	B100K	Alpha_RD901F-40-00D	coarse tune could be top or bottom
17	3	SW1, SW2, SW3	SWITCH SPDT	SPDT on-on	like a tayda A-4567
18	4	D2, D3, D4, D5	LED	3mm , color not important	orientation matters ... short leg to square pad, long leg to top/circle.
19	5	J1, J2, J3, J4, J5	Jack knobs	Thonkiconn and PJ-3001F	